

Figure 1

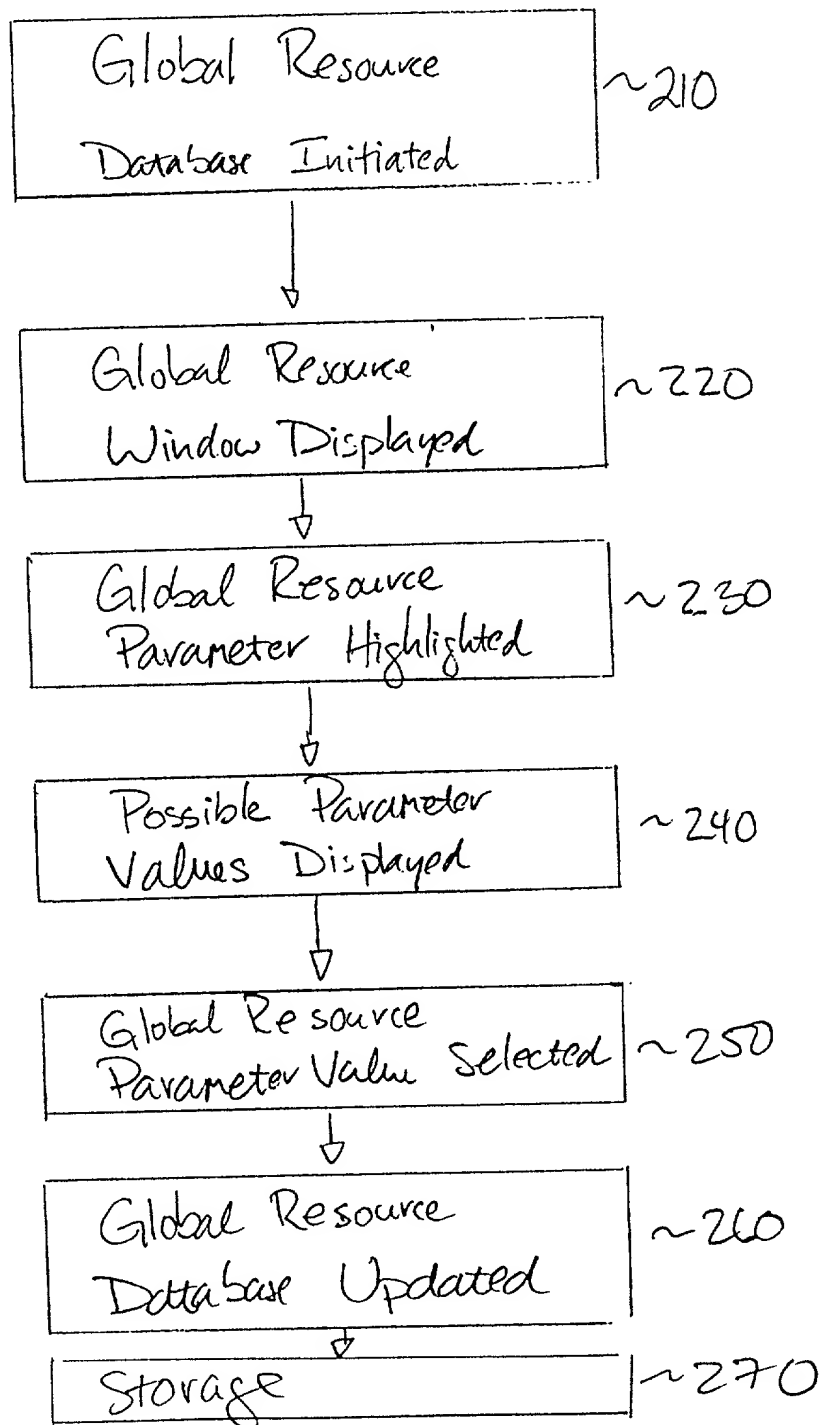


Figure 2A

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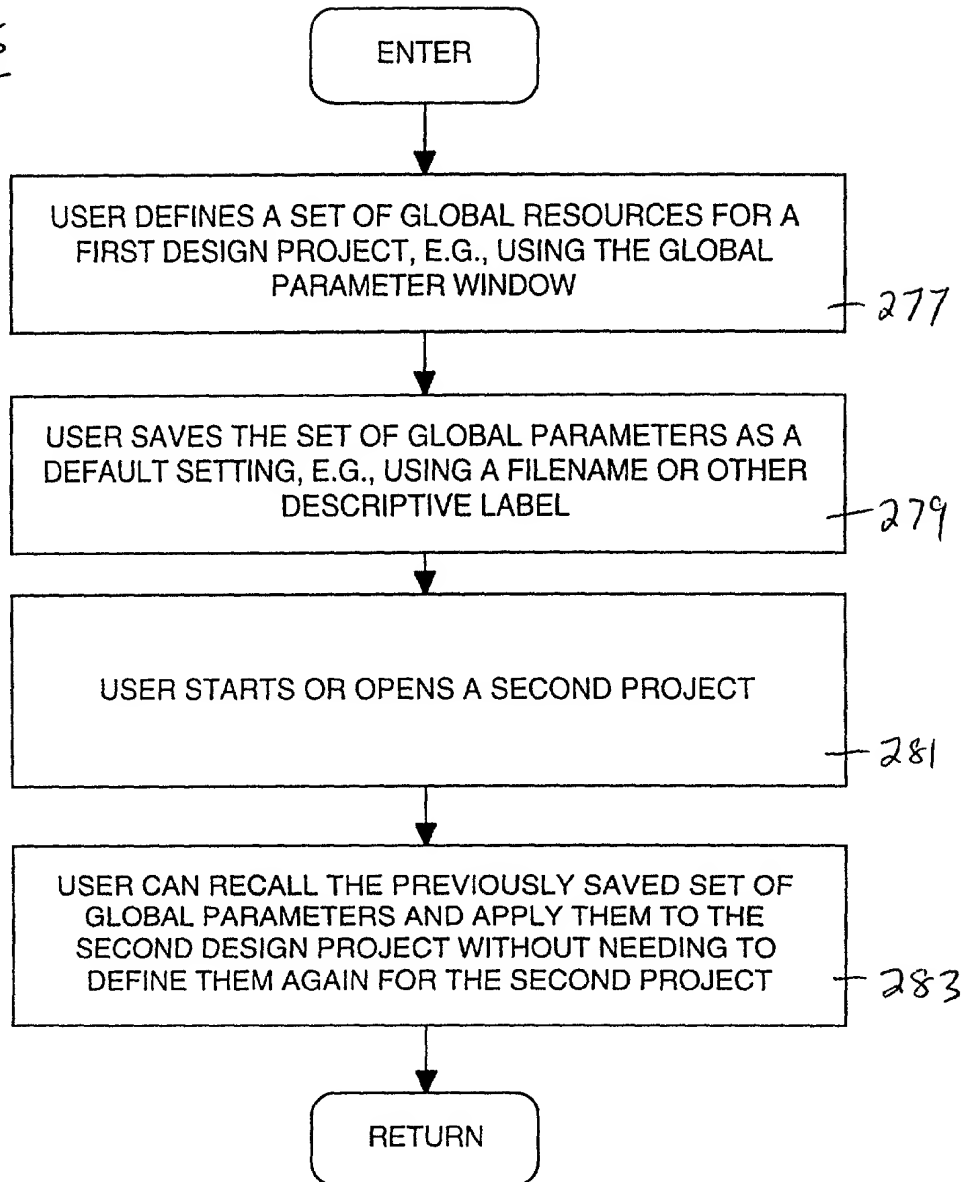


FIG. 2B

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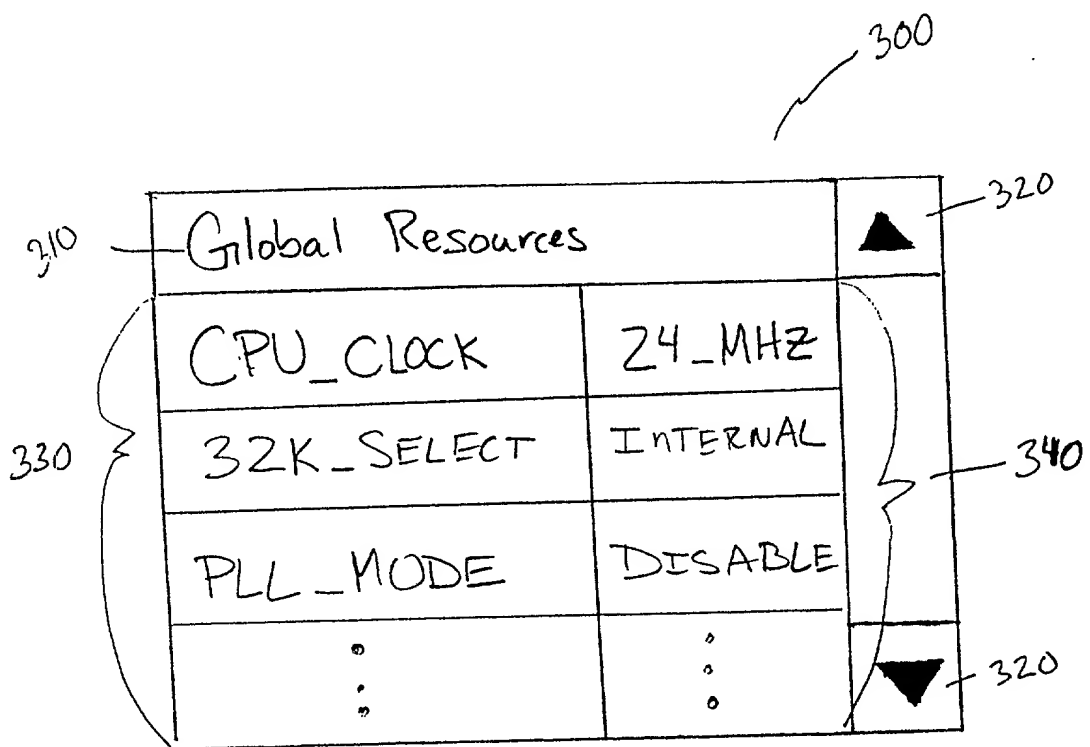


Figure 3

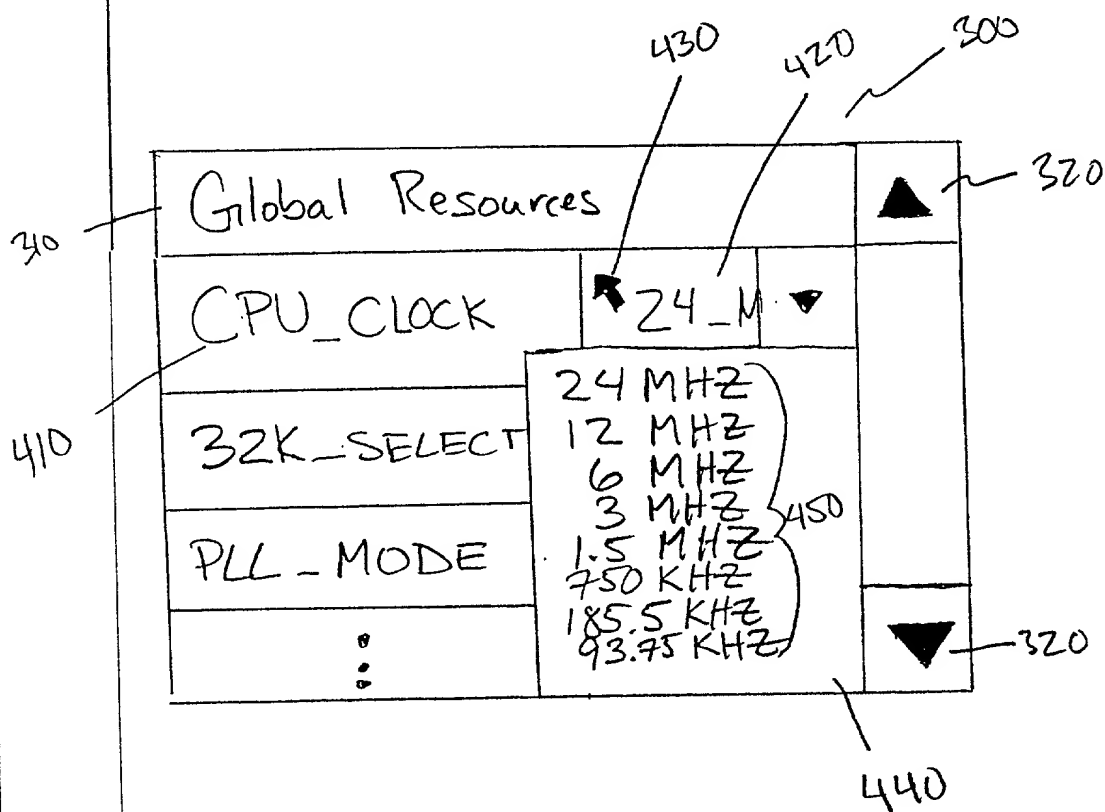


Figure 4

A hand-drawn block diagram titled "Global Resources" showing a vertical stack of configuration registers. The registers are labeled as follows:

- Global Resources**: The top register, with a callout 300 pointing to its right side.
- CPU_CLOCK**: The second register, with a callout 420 pointing to its right side. It contains the value "24_M" followed by a downward-pointing triangle.
- 32K_SELECT**: The third register, with a callout 410 pointing to its left side. It contains a list of frequencies: "24 MHz", "12 MHz", "16 MHz" (crossed out with diagonal lines), "3 MHz", "1.5 MHz", "750 KHz", "185.5 KHz", and "93.75 KHz". A bracket on the right side of this list is labeled 450. A callout 430 points to the "16 MHz" entry, and a callout 440 points to the "3 MHz" entry.
- PLL_MODE**: The fourth register, with a callout 440 pointing to its right side.
- ...**: An ellipsis indicating further registers.

On the right side of the diagram, there are two upward-pointing triangles and one downward-pointing triangle, each with a callout 320 pointing to it. A callout 500 points to the right side of the "32K_SELECT" register.

Figure 5

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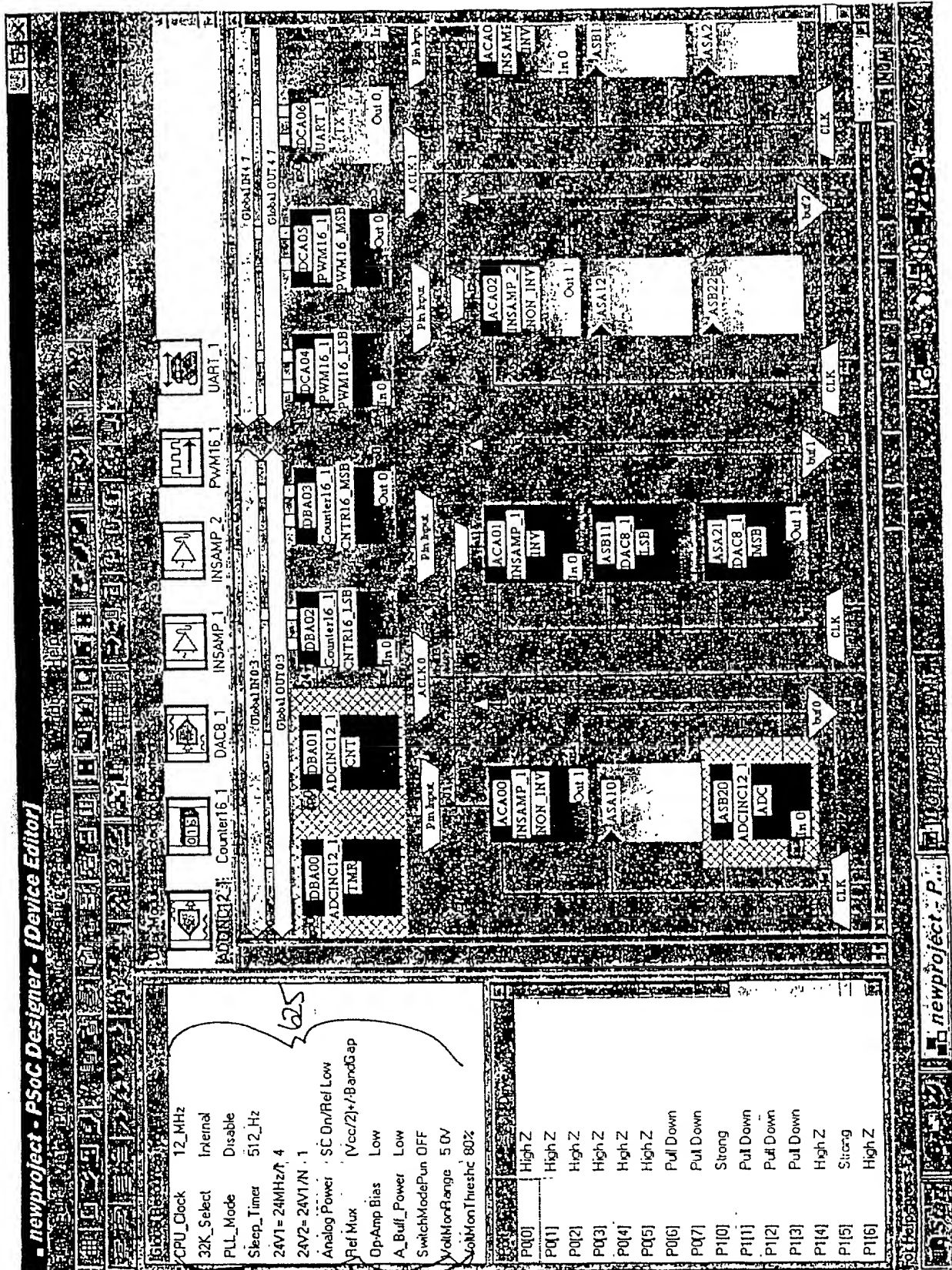


Fig. 6

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